

# Notice of Allowability

Application No.

10/671,642

Examiner

Jasmine J Clark

Applicant(s)

SUTHERLAND ET AL.

Art Unit

2815

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 08/16/04.
2. ☒ The allowed claim(s) is/are 1-12, 25-36; renumbered to 1-24.
3. ☒ The drawings filed on 26 September 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 09/26/03
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

**JASMINE CLARK  
PRIMARY EXAMINER**

***Information Disclosure Statement***

1. The Information Disclosure Statement filed 09/26/03 has been fully considered.

***References Cited***

2. Ma et al. (US 6,586,836 B1) show a structure of a semiconductor device comprising (see Figs. 5C and 6) a first die, eg, 102 disposed on one substrate, and a second die 102' disposed on another substrate, where the first die and second die are attached to each other with an adhesive layer 164. However, Ma fails to teach wherein the first semiconductor die and the second semiconductor die are arranged active face to active face so that signal pads on the first die overlap with signal pads on the second semiconductor die. Heo (US 6,555,917 B1) shows a first semiconductor die and a second semiconductor die arranged active face to active face, but fails to teach wherein the first semiconductor die and the second semiconductor die are pressed together between a first substrate and a second substrate so that the front face of the first substrate is in contact with the back face of the first semiconductor die and the front face of the second substrate is in contact with the back face of the second semiconductor die. Please also see Hofstee et al. (US 6,507,115 B2), Wojnarowski et al. (US 5,703,400), Nicewarner et al. (US 5,491,612), Masuda et al. (US 6,555,918 B2), and Song et al. (US 6,552,423 B2).

***Allowable Subject Matter***

3. Claims 1-12, 25-36 are allowed.

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The following is an examiner's statement of reasons for allowance: the prior art(s) fail to teach and/or suggest the limitations as set forth in claims 1 and 25.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Telephone Inquiry Contacts***

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jasmine J Clark whose telephone number is (571) 272-1726. The examiner can normally be reached on Flex.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jjbc/09/02/04

**JASMINE CLARK**  
**PRIMARY EXAMINER**  
